A Reconfigurable All-Optical Logic Device Using Blue and Red Shift

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Motivation

- High-speed all-optical logic gates are key elements to realize all-optical signal processing functions.

- Reconfigurable multi-logic gates are preferred as they can provide a more flexible set of networking functions.

- The existing reconfigurable multi-logic gate designs have already been proposed have several shortcomings:
  - Increasing the cost and complexity of the system by adding an additional continuous wave (CW) input or clock signal.
  - The operation is complicated by changing the parameters of input data signals.
Paper Review


Operation Principle

Fig. 1. Illustrating the principle of operation (Signal A acts as a pump and signal B acts as a probe). HNLF: highly nonlinear fiber, BPF: bandpass filter.

One lobe at $\lambda_c$ is identical to the probe spectrum before XPM which is formed by the logical zeros of pump A, and a second lobe at $\lambda_c'$ is a spectrally shifted replica of the probe spectrum, which is formed by the logical ones of pump A.
Operation Principle

Fig. 2. Illustrating the principle of operation (Signal A acts as a probe and signal B acts as a pump).

The second lobe can be red-shifted or blue-shifted, depending on which edge of the pump signal is superimposed on the probe signal.
Experimental Setup

The HNLF has a length of 1007 m, nonlinearity coefficient of 12.5 W$^{-1}$·km$^{-1}$, chromatic dispersion of 0.69 ps/nm·km and dispersion slope of 0.0074 ps/nm$^2$·km at 1550.0 nm.

Fig. 3. Experimental setup. TMLL: tunable mode-locked laser, PC: polarization controller, EOM: electrooptical modulator, EDFA: erbium-doped fiber amplifier, ODL: optical delay line, OC: optical circulator, HNLF: highly nonlinear fiber, BPF: bandpass filter, VOA: variable optical attenuator.
Experimental Setup

For the XOR gate, $A \cdot \overline{B}$ and $\overline{A} \cdot B$ are used after BPF1 and BPF2, respectively.

For the AND gate, $A \cdot B$ can be achieved after BPF1, after BPF2, or at the combined output.

For the OR gate, $A \cdot \overline{B}$ and $B$ are used after BPF1 and BPF2, respectively.

**TABLE II**

**WAVELENGTH SETTINGS OF BPF1 AND BPF2 FOR DIFFERENT LOGIC OUTPUT**

<table>
<thead>
<tr>
<th>Logic</th>
<th>$\lambda_{BPF2}$</th>
<th>$\lambda_{BPF1}$</th>
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<tbody>
<tr>
<td>XOR</td>
<td>1541.40 nm</td>
<td>1555.08 nm</td>
</tr>
<tr>
<td>AND</td>
<td>1538.32 nm</td>
<td>1558.52 nm</td>
</tr>
<tr>
<td>OR</td>
<td>1541.40 nm</td>
<td>1555.08 nm</td>
</tr>
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Fig. 4. Experimental results. ER: extinction ratio.
Fig. 5. Original spectrum of (a) signal B and (b) signal A. Optical spectrum after HNLF of (c) combined probe A and pump B and (d) combined pump A and probe B.
Result 3

Fig. 6. BER measurements for multilogic operations and for input signals A and B.

Power penalties at a BER of $10^{-9}$ for XOR, AND, and OR are 5.9, -1.0, and 2.9 dB, respectively.
A larger XPM effect is desired so that two peaks on the broadened spectrum of the probe can be clearly distinguished. Then, a larger chirp induced by XPM is preferred.

\[ \Delta \nu_{\text{max}} = \frac{\gamma_1 P_2 L}{\pi T_0 |\delta|} = \frac{\gamma_1 P_2 L_W}{\pi T_0} \]

Two way to maximize the XPM-induced chirp:

- Increase the power of pump \( P_2 \).
- Increase the walk-off length \( L_W \).

Increased \( P_2 \) \( \rightarrow \) overlap between signals \( \rightarrow \) a larger wavelength separation \( \rightarrow \) reduced \( L_W \)
Fig. 7. Spectrum when the wavelength separation between signals A and B is 5.5 nm. (a) signal B and (b) signal A. Optical spectrum after HNLF of (c) combined probe A and pump B and (d) combined pump A and probe B.
Discussion

Fig. 8. Spectrum broadening induced by XPM. (a) red shift (b) blue shift.

The broadened spectrum induced by XPM varies with the delay between the pump and the probe. When the wavelength of probe is larger than that of pump, the red shift is more suitable.
Conclusions

- A simple and novel reconfigurable all-optical multilogic device which uses a single HNLF without any additional input operating at 10 Gb/s is experimentally demonstrated.

- The experiment can obtain error-free performance, a receiver sensitivity less than -15.0 dBm for a BER = 10^{-9}, and power penalties of 5.9, -1.0, and 2.9 dB for XOR, AND, and OR operations, respectively.

- All gates are believed to operate successfully beyond 100 Gb/s based on the fast time response of the Kerr effect.
Thank you for your attention!

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